

# Serial Peripheral Interface e Verification Component

## Overview

The Serial Peripheral Interface(SPI) is the serial synchronous communication protocol developed by Motorola for M68HC11 family CPUs.

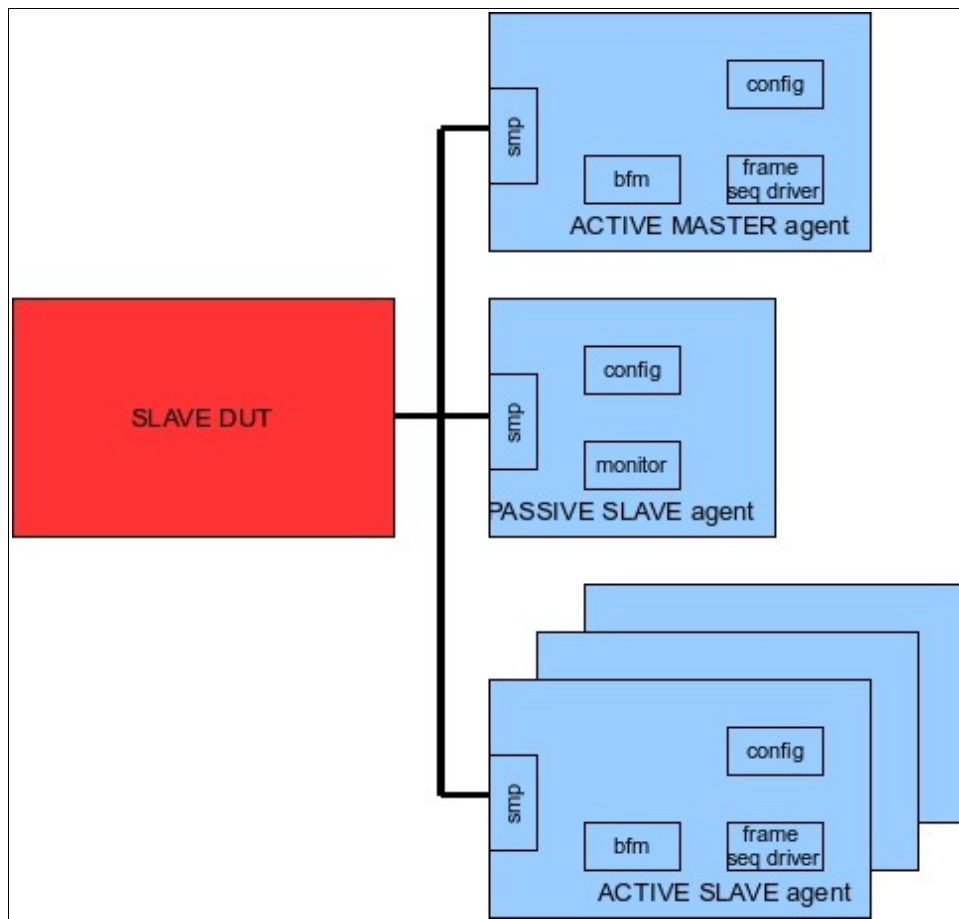
NoBug Consulting's SPI *e*VC is a fully documented, off the shelf *e* verification component(*e*VC) suitable for any DUT(device under test) that contains a SPI interface. Since with increasingly complex designs verification can take up to 70% of development time, *e*VCs provide tremendous cost and time-to-market advantage.

All *e*VC behavior is compliant with M68HC11 Reference Manual.

## Features

- *e*RM compliant
- supports both Master and Slave functionality
- support for on-the-fly variable baud rate
- support for on-the-fly clock polarity selection
- support for on-the-fly CPHA selection(edge sampling/driving selection)
- full control over the generated frames using sequences and sequence drivers
- assembly of collected data into structures using built-in monitors
- functional coverage
- protocol checker
- simulator and HDL independent

## NoBug Consulting



Verification environment topology for a slave DUT(block level)

## Deliverables

- Documentation(usage guide, eDoc, release notes)
- e sources
- Example test cases
- Shell script for a quick demo

## About Nobug

NoBug([www.nobug.ro](http://www.nobug.ro)) is an expert digital design verification company that masters a full range of technologies (functional, formal, assertion-based) with a variety of tools. Our goal is to establish strategic alliances designed to help deliver a customer-centric, total solutions approach to solving problems, exploiting business opportunities and creating sustainable competitive advantage for the customers.